



$$f_{TX} = \frac{1}{2\pi\sqrt{L_{TX}C_{TX}}} \rightarrow C_{TX} = \frac{1}{(2\pi f_{TX})^2 L_{TX}}$$

C_ADJ = To be adjusted according to coil inductivity. Calculate C_TX with the given formula.
 C_ADJ = 2 x C_TX
 It's recommended to use C0G or NP0 ceramic capacitors
 Use dielectric strength Vr >= 50V capacitors for C7 + (C10 .. C22)

LEGEND

Component Size 0603
 Component Size 0402
 Net Name
 Component Placement Suggestion
 Optional Component

Project name	IPS2200_SINGLE_TEMPLATE_V40.PrjPcb	Version	4.0	Cannot open file I:_Position_Sensing\00
Sheet title	IPS2200_SINGLE_TEMPLATE_V40	Size	A3	
File name	IPS2200_SINGLE_TEMPLATE_V40.SchDoc			
Date	2/28/2023	SYS. APP. ENG. TEAM	Drawn by	B.GOMBOR
			Sheet	1 of 1

PCB Manufacturing requirements

Project Name	IPS2200_SINGLE_TEMPLATE_V40.PrjPcb
Number of copper layers	2
PCB Base material	FR-4
Final PCB Thickness	1.00mm +/-10%
Thickness of copper coating	35um
Final cover	ENIG
Minimal copper width	0.2mm
Minimal copper to copper distance	0.15mm
Via hole/pad diameter	0.3/0.6mm
Slotted holes	Yes, see milling paths
Panel size	103x66mm
Silk screen color	White on TOP
Solder mask color	Green

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IPS2200_SINGLE_TEMPLATE

Date: 6/4/2021
Project: IPS2200_SINGLE_TEMPLATE_V40.PrjPcb
Version 4.0
Content: Schematic
Sensor PCB Layout
Sensor PCB 3D Model
Bill of Materials
PCB Manufacturing Requirements
Sensor PCB Layer Stack
Sensor PCB Placement
Target PCB Layout
Target PCB 3D Model
